ABSTRACT OF THE DISCLOSURE

A method for driving a plasma display panel is provided in which a wall voltage at an interelectrode between a display electrode and an address electrode is 5 controlled without increasing contrast in preparation for addressing, so that reliability of addressing is improved. As an operation of initialization for controlling the wall voltage of a cell within a screen as a preparation for the addressing, a first blunt wave application is performed for generating discharge only in a previous non-lighted cell that was not lighted in a previous display, and a second blunt wave application is performed for generating discharge in each of the previous non-lighted cell and a previous lighted cell that was lighted in the previous 15 display.

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